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1 Security: Analyzing and modeling encryption overhead for sensor network nodes

Prasanth Ganesan, Ramnath Venugopalan, Pushkin Peddabachagari, Alexander Dean, Frank Mueller, Mihail Sichitiu

September 2003 **Proceedings of the 2nd ACM international conference on Wireless sensor networks and applications**

Full text available: pdf(254.57 KB) Additional Information: full citation, abstract, references, index terms

Recent research in sensor networks has raised security issues for small embedded devices. Security concerns are motivated by the deployment of a large number of sensory devices in the field. Limitations in processing power, battery life, communication bandwidth and memory constrain the applicability of existing cryptography standards for small embedded devices. A mismatch between wide arithmetic for security (32 bit word operations) and embedded data bus widths (often only 8 or 16 bits) combined ...

Keywords: analysis, embedded systems, encryption overhead, model, sensor networks

2 SPINS: security protocols for sensor networks

Adrian Perrig, Robert Szewczyk, J. D. Tygar, Victor Wen, David E. Culler

September 2002 **Wireless Networks**, Volume 8 Issue 5

Full text available: pdf(213.37 KB) Additional Information: full citation, abstract, references, citings, index terms

Wireless sensor networks will be widely deployed in the near future. While much research has focused on making these networks feasible and useful, security has received little attention. We present a suite of security protocols optimized for sensor networks: SPINS. SPINS has two secure building blocks: SNEP and μTESLA. SNEP includes: data confidentiality, two-party data authentication, and evidence of data freshness. μTESLA provides authenticated broadcast for severely resource-constrained ...

Keywords: MANET, authentication of wireless communication, cryptography, mobile ad hoc networks, secrecy and confidentiality, secure communication protocols, sensor networks

3 SPINS: security protocols for sensor netowrks

Adrian Perrig, Robert Szewczyk, Victor Wen, David Culler, J. D. Tygar

July 2001 **Proceedings of the 7th annual international conference on Mobile**

computing and networking

Full text available: pdf(242.17 KB) Additional Information: full citation, abstract, references, citings, index terms

As sensor networks edge closer towards wide-spread deployment, security issues become a central concern. So far, much research has focused on making sensor networks feasible and useful, and has not concentrated on security.

We present a suite of security building blocks optimized for resource-constrained environments and wireless communication. SPINS has two secure building blocks: SNEP and &mgr;TESLA SNEP provides the following important baseline security primitives: Data confidentia ...

4 A survey of processors with explicit multithreading

Theo Ungerer, Borut Robič, Jurij Šilc

March 2003 **ACM Computing Surveys (CSUR)**, Volume 35 Issue 1

Full text available: pdf(920.16 KB) Additional Information: full citation, abstract, references, index terms

Hardware multithreading is becoming a generally applied technique in the next generation of microprocessors. Several multithreaded processors are announced by industry or already into production in the areas of high-performance microprocessors, media, and network processors.A multithreaded processor is able to pursue two or more threads of control in parallel within the processor pipeline. The contexts of two or more threads of control are often stored in separate on-chip register sets. Unused i ...

Keywords: Blocked multithreading, interleaved multithreading, simultaneous multithreading

5 Embedded applications: Encryption overhead in embedded systems and sensor network nodes: modeling and analysis

Ramnath Venugopalan, Prasanth Ganesan, Pushkin Peddabachagari, Alexander Dean, Frank Mueller, Mihail Sichitiu

October 2003 **Proceedings of the international conference on Compilers, architectures and synthesis for embedded systems**

Full text available: pdf(293.59 KB) Additional Information: full citation, abstract, references, index terms

Recent research in sensor networks has raised issues of security for small embedded devices. Security concerns are motivated by the deployment of a large number of sensory devices in the field. Limitations in processing power, battery life, communication bandwidth and memory constrain the applicability of existing cryptography standards for small embedded devices. A mismatch between wide arithmetic for security (32 bit word operations) and embedded data bus widths (often only 8 or 16 bits) combi ...

Keywords: embedded systems, encryption, security, sensor networks

6 Energy-aware design of embedded memories: A survey of technologies, architectures, and optimization techniques

Luca Benini, Alberto Macii, Massimo Poncino

February 2003 **ACM Transactions on Embedded Computing Systems (TECS)**, Volume 2 Issue 1

Full text available: pdf(288.44 KB) Additional Information: full citation, abstract, references, index terms

Embedded systems are often designed under stringent energy consumption budgets, to limit heat generation and battery size. Since memory systems consume a significant amount of energy to store and to forward data, it is then imperative to balance power

consumption and performance in memory system design. Contemporary system design focuses on the trade-off between performance and energy consumption in processing and storage units, as well as in their interconnections. Although memory design is as ...

Keywords: Embedded systems, embedded memories, integration, memories, nonvolatile, system-on-a-chip, volatile

7 Intrusion detection: Randomized instruction set emulation to disrupt binary code injection attacks

Elena Gabriela Barrantes, David H. Ackley, Trek S. Palmer, Darko Stefanovic, Dino Dai Zovi

October 2003 **Proceedings of the 10th ACM conference on Computer and communication security**

Full text available: pdf(160.71 KB) Additional Information: full citation, abstract, references, citings, index terms

Binary code injection into an executing program is a common form of attack. Most current defenses against this form of attack use a 'guard all doors' strategy, trying to block the avenues by which execution can be diverted. We describe a complementary method of protection, which disrupts foreign code execution regardless of how the code is injected. A unique and private machine instruction set for each executing program would make it difficult for an outsider to design binary attack code against ...

Keywords: automated diversity, emulation, information hiding, language randomization, obfuscation, security

8 Exploiting ILP in page-based intelligent memory

Mark Oskin, Justin Hensley, Diana Keen, Frederic T. Chong, Matthew Farrens, Aneet Chopra

November 1999 **Proceedings of the 32nd annual ACM/IEEE international symposium on Microarchitecture**

Full text available: pdf(1.35 MB) Additional Information: full citation, abstract, references, index terms

Publisher Site

This study compares the speed, area, and power of different implementations of Active Pages [OCS98], an intelligent memory system which helps bridge the growing gap between processor and memory performance by associating simple functions with each page of data. Previous investigations have shown up to 1000X speedups using a block of reconfigurable logic to implement these functions next to each sub-array on a DRAM chip. In this study, we show that instruction-level parallelism, n ...

9 A cost-effective, high-bandwidth storage architecture

Garth A. Gibson, David F. Nagle, Khalil Amiri, Jeff Butler, Fay W. Chang, Howard Gobioff, Charles Hardin, Erik Riedel, David Rochberg, Jim Zelenka

October 1998 **Proceedings of the eighth international conference on Architectural support for programming languages and operating systems**, Volume 32 , 33 Issue 5 , 11

Full text available: pdf(1.67 MB) Additional Information: full citation, abstract, references, citings, index terms

This paper describes the Network-Attached Secure Disk (NASD) storage architecture, prototype implementations oj NASD drives, array management for our architecture, and three, filesystems built on our prototype. NASD provides scalable storage bandwidth without the cost of servers used primarily, for transferring data from peripheral networks (e.g. SCSI) to client networks (e.g. ethernet). Increasing datuset sizes, new attachment technologies, the convergence of peripheral and interprocessor switc ...

10 Systems, platforms, and applications: MANTIS: system support for multimodAl NeTworks of in-situ sensors

H. Abrach, S. Bhatti, J. Carlson, H. Dai, J. Rose, A. Sheth, B. Shucker, J. Deng, R. Han

September 2003 **Proceedings of the 2nd ACM international conference on Wireless sensor networks and applications**

Full text available: pdf(424.53 KB) Additional Information: full citation, abstract, references, citings, index terms

The *MANTIS M*ultimod*A*l system for *NeT*works of In-situ wireless Sensors provides a new multithreaded embedded operating system integrated with a general-purpose single-board hardware platform to enable flexible and rapid prototyping of wireless sensor networks. The key design goals of MANTIS are ease of use, i.e. a small learning curve that encourages novice programmers to rapidly prototype novel sensor networking applications in software and hardware, as well as flexibility, ...

Keywords: GPS, dynamic reprogramming, lightweight, multimodal prototyping, operating systems, wireless sensor networks

11 Wide-area monitoring of mobile objects: Implementing software on resource-constrained mobile sensors: experiences with Impala and ZebraNet

Ting Liu, Christopher M. Sadler, Pei Zhang, Margaret Martonosi

June 2004 **Proceedings of the 2nd international conference on Mobile systems, applications, and services**

Full text available: pdf(3.14 MB) Additional Information: full citation, abstract, references, index terms

ZebraNet is a mobile, wireless sensor network in which nodes move throughout an environment working to gather and process information about their surroundings[10]. As in many sensor or wireless systems, nodes have critical resource constraints such as processing speed, memory size, and energy supply; they also face special hardware issues such as sensing device sample time, data storage/access restrictions, and wireless transceiver capabilities. This paper discusses and evaluates ZebraNet's syst ...

Keywords: event handling, middleware system, network communications, operation scheduling, sensor networks

12 Work-in-progress session on innovative topics: First results with eBlocks: embedded systems building blocks

Susan Cotterell, Frank Vahid, Walid Najjar, Harry Hsieh

October 2003 **Proceedings of the 1st IEEE/ACM/IFIP international conference on Hardware/software codesign & system synthesis**

Full text available: pdf(433.19 KB) Additional Information: full citation, abstract, references, index terms

We describe our first efforts to develop a set of off-the-shelf hardware components that ordinary people could connect to build a simple but useful class of embedded systems. The class of systems, which we call monitor/control systems, is composed primarily of sensors - light, motion, sound, contact, and other types - and output devices - light-emitting diodes, beeping speakers, or even electric relays that control electric appliances like lamps. For example, one monitor/control system would det ...

Keywords: embedded systems, intelligent homes, networks

13 Logical and physical design issues for smart card databases

Cristiana Bolchini, Fabio Salice, Fabio A. Schreiber, Letizia Tanca

July 2003 **ACM Transactions on Information Systems (TOIS)**, Volume 21 Issue 3

Full text available: pdf(1.12 MB) Additional Information: full citation, abstract, references, citings, index terms

The design of very small databases for smart cards and for portable embedded systems is deeply constrained by the peculiar features of the physical medium. We propose a joint approach to the logical and physical database design phases and evaluate several data structures with respect to the performance, power consumption, and endurance parameters of read/program operations on the Flash-EEPROM storage medium.

Keywords: Design methodology, access methods, data structures, flash memory, personal information systems, smart card

14 Embedded hardware design case studies: Design flow for HW / SW acceleration transparency in the thumbpod secure embedded system

David Hwang, Bo-Cheng Lai, Patrick Schaumont, Kazuo Sakiyama, Yi Fan, Shenglin Yang, Alireza Hodjat, Ingrid Verbauwhede

June 2003 **Proceedings of the 40th conference on Design automation**

Full text available: pdf(250.69 KB) Additional Information: full citation, abstract, references, index terms

This paper describes a case study and design flow of a secure embedded system called ThumbPod, which uses cryptographic and biometric signal processing acceleration. It presents the concept of HW/SW acceleration transparency, a systematic method to accelerate Java functions in both software and hardware. An example of acceleration transparency for a Rijndael encryption function is presented. The embedded prototype hardware platform is also described. Acceleration transparency yields software and ...

15 Heap compression for memory-constrained Java environments

G. Chen, M. Kandemir, N. Vijaykrishnan, M. J. Irwin, B. Mathiske, M. Wolczko

October 2003 **ACM SIGPLAN Notices , Proceedings of the 18th ACM SIGPLAN conference on Object-oriented programing, systems, languages, and applications**, Volume 38 Issue 11

Full text available: pdf(2.14 MB) Additional Information: full citation, abstract, references, citings, index terms

Java is becoming the main software platform for consumer and embedded devices such as mobile phones, PDAs, TV set-top boxes, and in-vehicle systems. Since many of these systems are memory constrained, it is extremely important to keep the memory footprint of Java applications under control.The goal of this work is to enable the execution of Java applications using a smaller heap footprint than that possible using current embedded JVMs. We propose a set of memory management strategies to reduce h ...

Keywords: Java virtual machine, garbage collection, heap, memory compression

16 MMLite: a highly componentized system architecture

Johannes Helander, Alessandro Forin

September 1998 **Proceedings of the 8th ACM SIGOPS European workshop on Support for composing distributed applications**

Full text available: pdf(1.01 MB) Additional Information: full citation, citings, index terms

17 Strategic directions in storage I/O issues in large-scale computing

Garth A. Gibson, Jeffrey Scott Vitter, John Wilkes

December 1996 **ACM Computing Surveys (CSUR)**, Volume 28 Issue 4

Full text available: pdf(465.35 KB) Additional Information: full citation, references, citings, index terms

18 Microprocessor applications in the nuclear industry

C. Dwayne Ethiridge

April 1980 **ACM SIGCAS Computers and Society**, Volume 10 Issue 3-4

Full text available: pdf(986.50 KB) Additional Information: full citation, abstract, references

Microprocessors in the nuclear industry, particularly at the los Al amos Scientific Laboratory, have been and are being utilized in a wide variety of applications ranging from data acquistion and control for basic physics research to monitoring special nuclear material in long-term storage. Microprocessor systems have been developed to support weapons diagnostics measurements during undergorund weapons testing at the Nevada Test Site. Multiple single-component microcomputers are now controlling ...

19 Special session on mobile computing #3: Implementing the TEA algorithm on sensors

Shuang Liu, Olga V. Gavrylyako, Phillip G. Bradford

April 2004 **Proceedings of the 42nd annual Southeast regional conference**

Full text available: pdf(510.44 KB) Additional Information: full citation, abstract, references

Sensors are tiny computers with limited computational capability and physical resources. The implementation of secure protocols for sensor network is a big challenge. In order to provide high security for sensor networks, it is very important to choose a small, efficient and effiective encryption algorithm as a security primitive. The TEA (Tiny Encryption Algorithm) is an efficient algorithm that requires little memory and resources. These features make the TEA a good candidate for security mech ...

Keywords: TEA, TinyOS, motes, security, sensor network

20 Information leakage from optical emanations

Joe Loughry, David A. Umphress

August 2002 **ACM Transactions on Information and System Security (TISSEC)**, Volume 5 Issue 3

Full text available: pdf(382.77 KB) Additional Information: full citation, abstract, references, index terms

A previously unknown form of compromising emanations has been discovered. LED status indicators on data communication equipment, under certain conditions, are shown to carry a modulated optical signal that is significantly correlated with information being processed by the device. Physical access is not required; the attacker gains access to all data going through the device, including plaintext in the case of data encryption systems. Experiments show that it is possible to intercept data under ...

Keywords: COMINT, COMSEC, EMSEC, SIGINT, TEMPEST, communication, compromising emanations, covert channel, encryption, fiber optics, information displays, light emitting diode (LED)

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Wacks, K.P.;
Consumer Electronics, IEEE Transactions on , Volume: 37 , Issue: 2 , May 19
Pages:168 - 174

[Abstract] [PDF Full-Text (484 KB)] **IEEE JNL**

2 **A new access persistence control mechanism at MAC level for data wireless cellular networks**
Ajib, W.; Godlewski, P.;
Personal, Indoor and Mobile Radio Communications, 2000. PIMRC 2000. The IEEE International Symposium on , Volume: 2 , 18-21 Sept. 2000
Pages:1180 - 1184 vol.2

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Liu, M.; Papantoni-Kazakos, P.;
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Ferreyra, P.A.; Marques, C.A.; Velazco, R.; Calvo, O.;
Radiation and Its Effects on Components and Systems, 2001. 6th European Conference on , 10-14 Sept. 2001
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[Abstract] [PDF Full-Text (360 KB)] **IEEE CNF**

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Piorkowski, J.; Hannan, D.; Cooper, B.;
Military Communications Conference Proceedings, 1999. MILCOM 1999. IEEE , Volume: 2 , 31 Oct.-3 Nov. 1999
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